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JE 4946

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL. NUM	10008800	FILING DATE	11/06/2001	CLASS	257	SUBCLASS		GAU	2815	EXAMINER	
<b>**APPLICANTS:</b> Gerber Mark; O'Connor Shawn; Thompson Trent											
<b>**CONTINUING DATA VERIFIED:</b> <i>no AA 10/10/02</i>											
<b>**FOREIGN APPLICATIONS VERIFIED:</b> <i>no AA 10/10/02</i>											
PUBLISH <input checked="" type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/> RESCIND <input type="checkbox"/>											
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no											
35 USC 110 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no											
Verified and Acknowledged Examiner's initials <i>AR</i>											
ATTORNEY DOCKET NO SC11588TK											
TITLE : Semiconductor package device and method of formation and testing											

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
Assistant Examiner		Total Claims	Print Claim for O.G.
Primary Examiner		DRAWING	
PREPARED FOR ISSUE		Sheets Drwg.	Figs. Drwg.
Application Examiner		Print Fig.	
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